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Appl. No. 10/707,647
Amdt. dated August 23, 2006
Reply to Office action of June 16, 2006

REMARKS/ARGUMENTS**1. Objection to the specification and the claims:**

The specification and the claims are objected to due to informalities.
Appropriate correction is required.

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Response:

The specification and the claims have been corrected to correct the noted informalities. Regarding claims 7 and 17, the phrase "first terminal" finds antecedent basis in claims 1 and 9, respectively. Acceptance of the corrected specification and claims is respectfully requested.

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2. Rejection of claims 1-7 under 35 U.S.C. 102(b):

Claims 1-7 are rejected under 35 U.S.C. 102(b) as being anticipated by Savelli (US 6,133,777).

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Response:

Claim 1 has been amended to overcome this rejection. Claim 1 is now directed to a high power auto selecting circuit, and recites that a first transistor of the first transistor is electrically coupled to a first power and a first terminal of the second transistor is electrically coupled to a second power. This amendment is supported in Fig. 2 of the instant application, which shows that a source of the first transistor 46 is connected to power V_{PP} and a source of the second transistor 48 is connected to power V_{DD} . In this way, the high voltage selecting module 44 receives voltages from two different external power sources and automatically outputs an output voltage according to the higher of the voltages output by the two power sources.

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On the other hand, Savelli teaches a first transistor 3 receiving a voltage from a

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5 first switch circuit 1 and a second transistor 4 receiving a voltage from a second switch circuit 2, but does not teach that the first and second transistors 3, 4 receive voltages from first and second external power sources. Therefore, the amended claim 1 is patentable over Savelli. Claims 2-7 are dependent on claim 1, and should be allowed if claim 1 is allowed. Reconsideration of claims 1-7 is therefore respectfully requested.

3. Rejection of claims 8-16 under 35 U.S.C. 103(a):

10 Claims 8-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Savelli (US 6,133,777) in view of Seki (US 5,122,692).

Response:

15 Claim 8 has been amended to include the limitations of original claim 9 and claim 9 has been cancelled. Additionally, claim 8 also recites that a first transistor of the first transistor is electrically coupled to a first power and a first terminal of the second transistor is electrically coupled to a second power. Neither Savelli nor Seki teaches these limitations. Therefore, the amended claim 8 should be allowable over the cited prior art. Claims 10-16 are dependent on claim 8, and should be allowed if claim 8 is allowed. Reconsideration of claims 8 and 10-16 is therefore respectfully requested.

4. Rejection of claims 17-19 under 35 U.S.C. 103(a):

25 Claims 17-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Savelli (US 6,133,777) in view of Seki (US 5,122,692).

Response:

Claims 17-19 are dependent on claim 8, and should be allowed if claim 8 is allowed. Reconsideration of claims 17-19 is therefore respectfully requested.

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In view of the claim amendments and the above arguments in favor of patentability, the applicant respectfully requests that a timely Notice of Allowance be issued in this case.

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5. Introduction to new claims 20-21:

New claims 20 and 21 each specify that voltages output by the first power and the second power are not related to one another in any way. That is, the voltage V_{PP} is not necessarily the logical opposite of voltage V_{DD} .

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On the other hand, Savelli teaches that the first and second switch circuits 1, 2 provide voltages to the first and second transistors 3, 4 that are logical opposites of each other. When one voltage is high, the other is low, and vice versa. Therefore, the cited prior art does not teach the limitations contained in new claims 20 and 21.

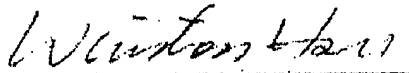
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Acceptance of claims 20 and 21 is respectfully requested.

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Sincerely yours,



Date: August 23, 2006

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